

<b>Notice of References Cited</b>	Application/Control No. 10/669,095	Applicant(s)/Patent Under Reexamination BUTTS, MICHAEL R.	
	Examiner Helen Rossoshek	Art Unit 2825	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,978,234 B1	12-2005	Battaline et al.	703/28
*	B	US-2006/0229856 A1	10-2006	Burrus et al.	703/011
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Platzner et al., "A distributed computer architecture for qualitative simulation based on a multi-DSP and FPGAs", 25-27 Jan. 1995, Parallel and Distributed Processing, 1995. Proceedings. Euromicro Workshop on, Page(s):311 - 318
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.